General specifications and features

Table 1 General

Technical data	**CPU 1214C AC/DC/Relay**	**CPU 1214C DC/DC/Relay**	**CPU 1214C DC/DC/DC**
Order number	6ES7 214-1BG31-0XB0	6ES7 214-1HG31-0XB0	6ES7 214-1AG31-0XB0
Dimensions W x H x D (mm)	110 x 100 x 75	110 x 100 x 75	110 x 100 x 75
Shipping weight	475 grams	435 grams	415 grams
Power dissipation	14 W	12 W	12 W
Current available (SM and CM bus)	1600 mA max. (5 VDC)	1600 mA max. (5 VDC)	1600 mA max. (5 VDC)
Current available (24 VDC)	400 mA max. (sensor power)	400 mA max. (sensor power)	400 mA max. (sensor power)
Digital input current consumption (24VDC)	4 mA/input used	4 mA/input used	4 mA/input used

Table 2 CPU features

Technical data		**Description**
User memory[1]	Work	75 Kbytes
	Load	4 Mbytes internal, expandable up to SD card size
	Retentive	10 Kbytes
On-board digital I/O		14 inputs/10 outputs
On-board analog I/O		2 inputs
Process image size		1024 bytes of inputs (I)/1024 bytes of outputs (Q)
Bit memory (M)		8192 bytes
Temporary (local) memory		• 16 Kbytes for startup and program cycle (including associated FBs and FCs) • 4 Kbytes for standard interrupt events including FBs and FCs • 4 Kbytes for error interrupt events including FBs and FCs
Signal modules expansion		8 SMs max.
SB, CB, BB expansion		1 max.
Communication module expansion		3 CMs max.
High-speed counters		6 total, see table Operation of the high-speed counter • Single phase: 3 at 100 kHz and 3 at 30 kHz clock rate, • Quadrature phase: 3 at 80 kHz and 3 at 20 kHz clock rate
Pulse outputs [2]		4
Pulse catch inputs		14

Technical data	Description
Time delay / cyclic interrupts	4 total with 1 ms resolution
Edge interrupts	12 rising and 12 falling (14 and 14 with optional signal board)
Memory card	SIMATIC Memory Card (optional)
Real time clock accuracy	+/- 60 seconds/month
Real time clock retention time	20 days typ./12 days min. at 40°C (maintenance-free Super Capacitor)

[1] The size of the user program, data, and configuration is limited by the available load memory and work memory in the CPU. There is no specific limit to the number of OB, FC, FB and DB blocks supported or to the size of a particular block; the only limit is due to overall memory size.

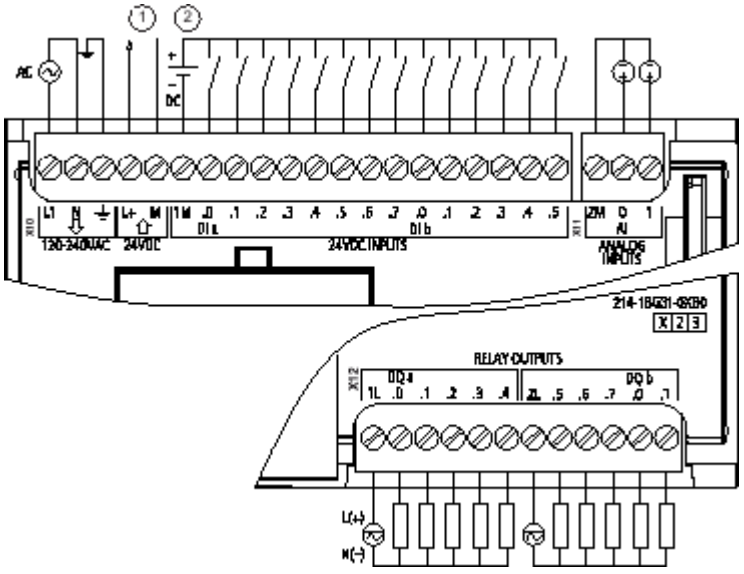
[2] For CPU models with relay outputs, you must install a digital signal board (SB) to use the pulse outputs.

Table 3 Performance

Type of instruction	Execution speed
Boolean	0.08 µs/instruction
Move Word	1.7 µs/instruction
Real math	2.3 µs/instruction

CPU 1214C Wiring Diagrams

Table 1 CPU 1214C AC/DC/Relay (6ES7 214-1BG31-0XB0)



① 24 VDC Sensor Power Out

For additional noise immunity, connect "M" to chassis ground even if not using sensor supply.

② For sinking inputs, connect "-" to "M" (shown).

For sourcing inputs, connect "+" to "M".

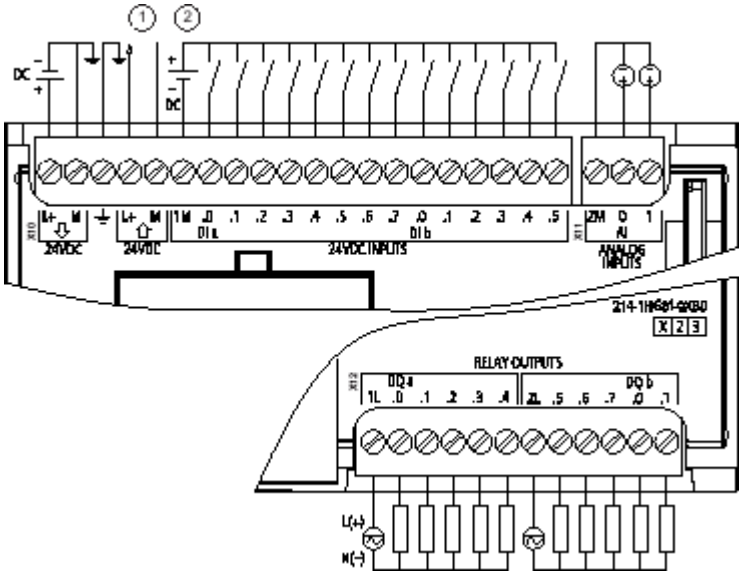
Note: X11 connectors must be gold. See Appendix C, Spare Parts for order number.

Table 2 Connector pin locations for CPU 1214C AC/DC/Relay (6ES7 214-1BG31-0XB0)

Pin	X10	X11 (gold)	X12
1	L1 / 120-240 VAC	2 M	1L
2	N / 120-240 VAC	AI 0	DQ a.0
3	Functional Earth	AI 1	DQ a.1
4	L+ / 24VDC Sensor Out	--	DQ a.2
5	M / 24VDC Sensor Out	--	DQ a.3
6	1M	--	DQ a.4
7	DI a.0	--	2L
8	DI a.1	--	DQ a.5
9	DI a.2	--	DQ a.6
10	DI a.3	--	DQ a.7
11	DI a.4	--	DQ b.0
12	DI a.5	--	DQ b.1
13	DI a.6	--	--
14	DI a.7	--	--
15	DI b.0	--	--
16	DI b.1	--	--

Pin	X10	X11 (gold)	X12
17	DI b.2	--	--
18	DI b.3	--	--
19	DI b.4	--	--
20	DI b.5	--	--

Table 3 CPU 1214C DC/DC/Relay (6ES7 214-1HG31-0XB0)



① 24 VDC Sensor Power Out

For additional noise immunity, connect "M" to chassis ground even if not using sensor supply.

② For sinking inputs, connect "-" to "M" (shown).

For sourcing inputs, connect "+" to "M".

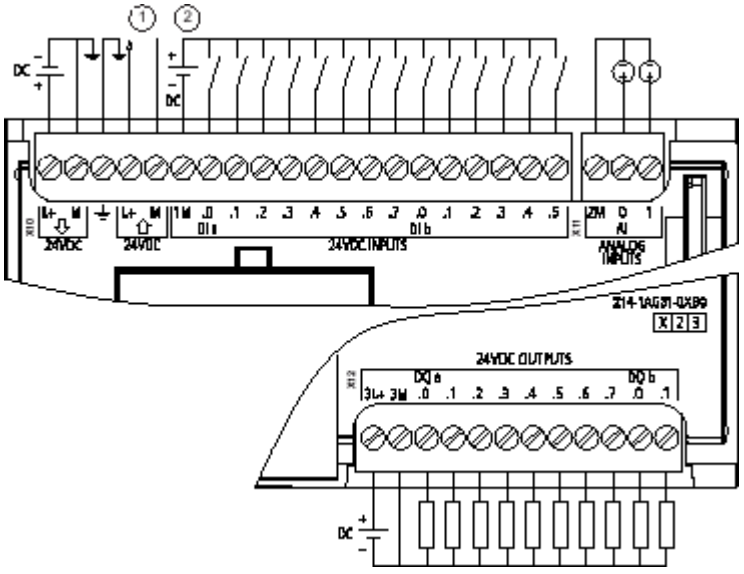
Note: X11 connectors must be gold. See Appendix C, Spare Parts for order number.

Table 4 Connector pin locations for CPU 1214C DC/DC/Relay (6ES7 214-1HG31-0XB0)

Pin	X10	X11 (gold)	X12
1	L+ / 24VDC	2 M	1L
2	M / 24VDC	AI 0	DQ a.0
3	Functional Earth	AI 1	DQ a.1
4	L+ / 24VDC Sensor Out	--	DQ a.2
5	M / 24VDC Sensor Out	--	DQ a.3
6	1M	--	DQ a.4
7	DI a.0	--	2L
8	DI a.1	--	DQ a.5
9	DI a.2	--	DQ a.6
10	DI a.3	--	DQ a.7
11	DI a.4	--	DQ b.0
12	DI a.5	--	DQ b.1

Pin	X10	X11 (gold)	X12
13	DI a.6	--	--
14	DI a.7	--	--
15	DI b.0	--	--
16	DI b.1	--	--
17	DI b.2	--	--
18	DI b.3	--	--
19	DI b.4	--	--
20	DI b.5	--	--

Table 5 CPU 1214C DC/DC/DC (6ES7 214-1AG31-0XB0)



① 24 VDC Sensor Power Out

For additional noise immunity, connect "M" to chassis ground even if not using sensor supply.

② For sinking inputs, connect "-" to "M" (shown).

For sourcing inputs, connect "+" to "M".

Note: X11 connectors must be gold. See Appendix C, Spare Parts for order number.

Table 6 Connector pin locations for CPU 1214C DC/DC/DC (6ES7 214-1AG31-0XB0)

Pin	X10	X11 (gold)	X12
1	L+ / 24VDC	2 M	3L+
2	M / 24VDC	AI 0	3M
3	Functional Earth	AI 1	DQ a.0
4	L+ / 24VDC Sensor Out	--	DQ a.1
5	M / 24VDC Sensor Out	--	DQ a.2
6	1M	--	DQ a.3
7	DI a.0	--	DQ a.4

Pin	X10	X11 (gold)	X12
8	DI a.1	--	DQ a.5
9	DI a.2	--	DQ a.6
10	DI a.3	--	DQ a.7
11	DI a.4	--	DQ b.0
12	DI a.5	--	DQ b.1
13	DI a.6	--	--
14	DI a.7	--	-
15	DI b.0	--	--
16	DI b.1	--	--
17	DI b.2	--	--
18	DI b.3	--	--
19	DI b.4	--	--
20	DI b.5	--	--

Note

Unused analog inputs should be shorted.

Sample time for the built-in analog ports of the CPU

Table 1 Sample time for built-in analog inputs of the CPU

Rejection frequency (Integration time selection)	Sample time
60 Hz (16.6 ms)	4.17 ms
50 Hz (20 ms)	5 ms
10 Hz (100 ms)	25 ms

Step response of the built-in analog inputs of the CPU

Table 1 Step Response (ms), 0V to 10V measured at 95%

Smoothing selection (sample averaging)	Rejection frequency (Integration time)		
	60 Hz	50 Hz	10 Hz
None (1 cycle): No averaging	50 ms	50 ms	100 ms
Weak (4 cycles): 4 samples	60ms	70 ms	200 ms
Medium (16 cycles): 16 samples	200 ms	240 ms	1150 ms
Strong (32 cycles): 32 samples	400 ms	480 ms	2300 ms
Sample time	**4.17 ms**	**5 ms**	**25 ms**

Analog inputs

Table 1 Analog inputs

Technical data	Description
Number of inputs	2
Type	Voltage (single-ended)
Full-scale range	0 to 10 V
Full-scale range (data word)	0 to 27648
Overshoot range	10.001 to 11.759 V
Overshoot range (data word)	27,649 to 32,511
Overflow range	11.760 to 11.852 V
Overflow range (data word)	32,512 to 32,767
Resolution	10 bits
Maximum withstand voltage	35 VDC
Smoothing	None, Weak, Medium, or Strong See the table for step response (ms) for the analog inputs of the CPU.
Noise rejection	10, 50, or 60 Hz
Impedance	≥100 KΩ
Isolation (field side to logic)	None
Accuracy (25°C / -20 to 60°C)	3.0% / 3.5% of full-scale
Cable length (meters)	100 m, shielded twisted pair

Digital inputs and outputs

Table 1 Digital inputs

Technical data	CPU 1214C AC/DC/Relay	CPU 1214C DC/DC/Relay	CPU 1214C DC/DC/DC
Number of inputs	14		
Type	Sink/Source (IEC Type 1 sink)		
Rated voltage	24 VDC at 4 mA, nominal		
Continuous permissible voltage	30 VDC, max.		
Surge voltage	35 VDC for 0.5 sec.		
Logic 1 signal (min.)	15 VDC at 2.5 mA		
Logic 0 signal (max.)	5 VDC at 1 mA		
Isolation (field side to logic)	500 VAC for 1 minute		
Isolation groups	1		
Filter times	0.2, 0.4, 0.8, 1.6, 3.2, 6.4, and 12.8 ms (selectable in groups of 4)		
HSC clock input rates (max.) (Logic 1 Level = 15 to 26 VDC)	Single phase: 100 KHz (Ia.0 to Ia.5) and 30 KHz (Ia.6 to Ib.5) Quadrature phase: 80 KHz (Ia.0 to Ia.5) and 20 KHz (Ia.6 to Ib.5)		
Number of inputs on simultaneously	• 7 (no adjacent points) at 60° C horizontal or 50° C vertical • 14 at 55° C horizontal or 45° C vertical		
Cable length (meters)	500 m shielded, 300 m unshielded, 50 m shielded for HSC inputs		

Table 2 Digital outputs

Technical data	CPU 1214C AC/DC/Relay and DC/DC/Relay	CPU 1214C DC/DC/DC
Number of outputs	10	10
Type	Relay, dry contact	Solid state - MOSFET (sourcing)
Voltage range	5 to 30 VDC or 5 to 250 VAC	20.4 to 28.8 VDC
Logic 1 signal at max. current	--	20 VDC min.
Logic 0 signal with 10 KΩ load	--	0.1 VDC max.
Current (max.)	2.0 A	0.5 A
Lamp load	30 W DC / 200 W AC	5 W
ON state resistance	0.2 Ω max. when new	0.6 Ω max.
Leakage current per point	--	10 µA max.
Surge current	7 A with contacts closed	8 A for 100 ms max.
Overload protection	No	No

Technical data	CPU 1214C AC/DC/Relay and DC/DC/Relay	CPU 1214C DC/DC/DC
Isolation (field side to logic)	1500 VAC for 1 minute (coil to contact) None (coil to logic)	500 VAC for 1 minute
Isolation resistance	100 MΩ min. when new	--
Isolation between open contacts	750 VAC for 1 minute	--
Isolation groups	2	1
Inductive clamp voltage	--	L+ minus 48 VDC, 1 W dissipation
Switching delay (Qa.0 to Qa.3)	10 ms max.	1.0 µs max., off to on 3.0 µs max., on to off
Switching delay (Qa.4 to Qb.1)	10 ms max.	50 µs max., off to on 200 µs max., on to off
Maximum relay switching frequency	1 Hz	--
Pulse Train Output rate (Qa.0 and Qa.2)	Not recommended [1]	100 KHz max., 2 Hz min. [2]
Lifetime mechanical (no load)	10,000,000 open/close cycles	--
Lifetime contacts at rated load	100,000 open/close cycles	--
Behavior on RUN to STOP	Last value or substitute value (default value 0)	
Number of outputs on simultaneously	• 5 (no adjacent points) at 60° C horizontal or 50° C vertical • 10 at 55° C horizontal or 45° C vertical	
Cable length (meters)	500 m shielded, 150 m unshielded	

[1] For CPU models with relay outputs, you must install a digital signal board (SB) to use the pulse outputs.

[2] Depending on your pulse receiver and cable, an additional load resistor (at least 10% of rated current) may improve pulse signal quality and noise immunity.

Timers, counters and code blocks supported by CPU 1214C

Table 1 Blocks, timers and counters supported by CPU 1214C

Element		Description
Blocks	Type	OB, FB, FC, DB
	Size	64 Kbytes
	Quantity	Up to 1024 blocks total (OBs + FBs + FCs + DBs)
	Address range for FBs, FCs, and DBs	1 to 65535 (such as FB 1 to FB 65535)
	Nesting depth	16 from the program cycle or start up OB; 4 from the time delay interrupt, time-of-day interrupt, cyclic interrupt, hardware interrupt, time error interrupt, or diagnostic error interrupt OB
	Monitoring	Status of 2 code blocks can be monitored simultaneously
OBs	Program cycle	Multiple: OB 1, OB 200 to OB 65535
	Startup	Multiple: OB 100, OB 200 to OB 65535
	Time-delay interrupts and cyclic interrupts	4[1] (1 per event): OB 200 to OB 65535
	Hardware interrupts (edges and HSC)	50 (1 per event): OB 200 to OB 65535
	Time error interrupts	1: OB 80
	Diagnostic error interrupts	1: OB 82
Timers	Type	IEC
	Quantity	Limited only by memory size
	Storage	Structure in DB, 16 bytes per timer
Counters	Type	IEC
	Quantity	Limited only by memory size
	Storage	Structure in DB, size dependent upon count type • SInt, USInt: 3 bytes • Int, UInt: 6 bytes • DInt, UDInt: 12 bytes

[1] Time-delay and cyclic interrupts use the same resources in the CPU. You can have only a total of 4 of these interrupts (time-delay plus cyclic interrupts). You cannot have 4 time-delay interrupts and 4 cyclic interrupts.

Table 2 Communication

Technical data	Description
Number of ports	1

Technical data	Description
Type	Ethernet
HMI device[1]	3
Programming device (PG)	1
Connections	• 8 for Open User Communication (active or passive): TSEND_C, TRCV_C, TCON, TDISCON, TSEND, and TRCV • 3 for server GET/PUT (CPU-to-CPU) S7 communication • 8 for client GET/PUT (CPU-to-CPU) S7 communication
Data rates	10/100 Mb/s
Isolation (external signal to PLC logic)	Transformer isolated, 1500 VAC, for short term event safety only
Cable type	CAT5e shielded

[1] The CPU provides dedicated HMI connections to support up to 3 HMI devices. (You can have up to 2 SIMATIC Comfort panels.) The total number of HMI is affected by the types of HMI panels in your configuration. For example, you could have up to three SIMATIC Basic panels connected to your CPU, or you could have up to two SIMATIC Comfort panels with one additional Basic panel.

Table 3 Power supply

Technical data		CPU 1214C AC/DC/Relay	CPU 1214C DC/DC/Relay	CPU 1214C DC/DC/DC
Voltage range		85 to 264 VAC	20.4 VDC to 28.8 VDC 22.0 VDC to 28.8 VDC for ambient temperature -20° C to 0° C	
Line frequency		47 to 63 Hz	--	
Input current (max. load)	CPU only	100 mA at 120 VAC 50 mA at 240 VAC	500 mA at 24 VDC	
	CPU with all expansion accessories	300 mA at 120 VAC 150 mA at 240 VAC	1500 mA at 24 VDC	
Inrush current (max.)		20 A at 264 VAC	12 A at 28.8 VDC	
Isolation (input power to logic)		1500 VAC	Not isolated	
Ground leakage, AC line to functional earth		0.5 mA max.	-	
Hold up time (loss of power)		20 ms at 120 VAC 80 ms at 240 VAC	10 ms at 24 VDC	
Internal fuse, not user replaceable		3 A, 250 V, slow blow		

Table 4 Sensor power

Technical data	CPU 1214C AC/DC/Relay	CPU 1214C DC/DC/Relay	CPU 1214C DC/DC/DC
Voltage range	20.4 to 28.8 VDC	L+ minus 4 VDC min. L+ minus 5 VDC min. for ambient temperature -20° C to 0° C	
Output current rating (max.)	400 mA (short circuit protected)		
Maximum ripple noise (<10 MHz)	< 1 V peak to peak	Same as input line	
Isolation (CPU logic to sensor power)	Not isolated		